

#7/Election  
4/29/02  
JS

PATENT APPLICATION  
Q63936

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Makoto NONAKA

Appln. No.: 09/823,752

Group Art Unit: 2815

Confirmation No.: 9106

Examiner: George C. ECKERT II

Filed: April 03, 2001

For: SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING THE SAME

RECEIVED  
APR 19 2002  
TECHNOLOGY CENTER 2800

RESPONSE TO RESTRICTION REQUIREMENT

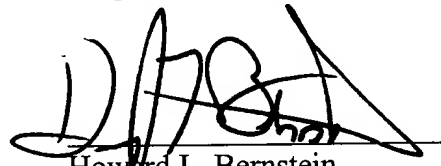
Commissioner for Patents  
Washington, D.C. 20231

Sir:

In response to the Office Action dated March 19, 2002 in which the Examiner has required a restriction, Applicants respond by electing Group I, claims 1-16, drawn to a semiconductor device.

Prompt and favorable action on the elected claims are now respectfully requested

Respectfully submitted,

  
Howard L. Bernstein  
Registration No. 25,665

SUGHRUE MION, PLLC  
2100 Pennsylvania Avenue, N.W.  
Washington, D.C. 20037-3213  
Telephone: (202) 293-7060  
Facsimile: (202) 293-7860  
Date: April 19, 2002